

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Atty. Docket No. (Opt.) CVC1530-3
Applicant Mehrdad M. Moslehi	
Application Number Unknown	Filed March 6, 2002
For Ultra High-Speed Chip Semiconductor Integrated Circuit Interconnect Structure And Fabrication Method Using Free-Space Dielectrics	
Group Art Unit Unknown	Examiner Unknown

J1040 U.S. PTO
 10/09/1795
 03/06/02

Box Patent Application

Assistant Commissioner for Patents
Washington, D.C. 20231

Certification Under 37 C.F.R. §1.10

I hereby certify that this document is being deposited with the United States Postal Service as Express Mail to Addressee in an envelope addressed to: Box Patent Application, Assistant Commissioner for Patents, Washington, D.C. 20231 on **March 6, 2002**. Express Mail Label No. EL911433316US

Janice Pampell
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Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the art listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified divisional application. Furthermore, pursuant to 37 C.F.R. §§ 1.97(g) and (h), no representation is made that a search has been made or that this art is material to patentability of the present application.

While Applicants believe no fees are due, if any fees are due, the Commissioner is hereby authorized to charge Deposit Account No. 50-0456 of Gray Cary Ware & Freidenrich LLP.

Applicants respectfully submit that the claims of Applicants' above-referenced patent application are patentably distinguishable from these references.

Respectfully submitted,

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Dated: March 6, 2002

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FORM PTO 1449 US Department of Commerce Patent and Trademark Office	ATTY DOCKET NO.: CVC1530-3	SERIAL NO.: Unknown
	APPLICANT(S): Mehrdad M. Moslehi	
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U.S. PATENT DOCUMENTS

EXAM. INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	A1	3,890,636	06/17/75	Seiki Harada, et al.	357	68	09/11/72
	A2	4,289,846	09/15/81	Parks, et al.	430	314	12/28/79
	A3	4,933,743	06/12/90	Thomas, et al.	357	71	08/14/89
	A4	5,117,276	05/26/92	Thomas, et al.	357	71	11/08/90
	A5	5,256,869	10/26/93	Lin, et al.	250	201.9	06/30/92
	A6	5,372,969	12/13/94	Moslehi	437	195	03/03/92
	A7	5,641,712	06/24/97	Grivna, et al.	438	624	08/07/95
	A8	6,146,985	11/14/00	Wollesen	438	618	05/04/98
	A9	5,798,559	08/25/98	Subhas Bothra, et al.	257	522	09/03/97
	A10	5,882,963	03/99	Kerber, et al.	438	619	
	A11	5,930,668	07/99	Gardner	438	624	
	A12	5,953,626	09/99	Hause, et al.	438	622	
	A13	6,091,149	07/00	Hause, et al.	257	758	

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION (YES/NO)
	B1	0 644 443 A1	09/12/94	European	GO2B	6/42	Yes
	B2	0 475 646 A2	08/30/91	European	HO1L	23/522	Yes

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages)

C1	<i>Solid State</i> , by Linda Gepperts, IEEE SPECTRUM, January 1998, pp. 23-8.
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EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.